IN THE SPECIFICATION

In the Abstract at page 30, lines 2 – 21, please change as indicated:

A baseband controller includes a ~~micro-sequencer~~ microsequencer that is formed to include special hardware resources and a configuration that facilitates using the micro-sequencer as a real-time baseband controller. The ~~inventive micro-sequencer~~ microsequencer includes a 72-bit correlator that may also be used as an accumulator~~,~~ ~~wherein the topology includes the correlator being~~ . The correlator is able to communicate with a 72-bit arithmetic logic unit ~~which, therefore, enables,~~ enabling the correlator to act as an accumulator, as well as a plurality of clocks and timers that facilitate the timing functionality ~~that is required~~ to satisfy Bluetooth specifications. More specifically, the ~~micro-sequencer~~ microsequencer includes at least four clocks and eight timers ~~in one embodiment of the present invention~~. The four clocks include an externally driven Bluetooth clock, an externally driven real-time clock, a native Bluetooth clock and a native real-time clock. The ~~micro-sequencer~~ microsequencer ~~of the described embodiment~~ further includes at least four registers for temporarily storing computational data. The storage registers ~~are made to~~ have different sizes for storing different sized packets of computational data.